

Application Note

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APM32 Series Tool Chain User Manual

Version: V1.1

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1 Introduction

For an MCU, the quality of the tool chain will become one of the keys to its market performance. When tool chain is used to develop programs, the functional completeness and interface friendliness of the tool chain will directly affect the user experience. Moreover, the performance of the chip also depends on the performance of the compiler to a large extent.

In order to enable users to use MCU to complete the design and verification of application system solutions, MCU manufacturers generally provide support in two ways: one is that MCU manufacturers provide a full set of platform tools through their own research and development, and the other is that customers seek the support of third-party general tools (e.g. IAR).

According to our own product characteristics and application field requirements, Geehy Semiconductor independently develops and builds a complete set of software and hardware development tools, which can help users shorten the product development cycle, improve product stability, and provide strong support for customers in the process of MCU application development.

This application note provides basic instructions for use of Geehy software and hardware tools, including the guidance for the connection and configuration operation process of Geehy-Link, APM32PROG, APM32 ISP Multiport Programmer and DFU Programmer.



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2 Geehy tool chain tutorial

- > Preparatory work
- > Hardware preparation
- > Geehy-Link
- > APM32 PROG
- > USB-to-TTL module
- > APM32F0/1/4 series MCU development board
- > Software preparation
- > Keil uVision5 (recommend version 5.25 and above)
- > IAR EW for Arm 9.20.2
- > APM32 PROG
- > ISP Multiport Programmer
- > DFUProgrammer
- > APM32F0/1/4 series MCU SDK

> The above Geehy related software can be downloaded from Geehy data ([APM32 MCU (geehy.com)] (https://geehy.com/support/apm32))



3 Geehy-Link tutorial

Geehy-LINK is a development tool integrating simulator and programmer, which can debug and simulate the APM32 full-range MCU products online in Keil, IAR and other integrated development environments. Support full-speed operation, single-step debugging, breakpoint setting and other debugging methods.

| Development Tools | |
|--|--|
| Geehy provides abundant flexible and easy-to-use to accelerate product development. | software and hardware development tools for u |
| | APM32 Moce Status |
| Emulator/Prog (GEEHY-LINK) | APM32 PROG Support offline and online programming |
| Support SWD and JTAG programming | Support JTAG and SWD programming |
| Support target board power supply through button controlling | Support win7/8 /10 drive-free use |

Figure 1 Geehy-Link Link on the Official Website

Use the USB cable to connect Geehy-LINK to the computer. After they are connected successfully, Geehy DAP USB Device will appear on the device manager, as shown in the following figure



Figure 2 Geehy DAP USB Device in Device Manager



3.1 Configure Geehy-Link debugger under Keil MDK

3.1.1 Pack supports installation

In the target environment, you need to prepare Keil uVisin5 (recommend version 5.25 and above) environment in advance, and install the Pack of APM32F0/1/4 series MCU that needs to be debugged. The installation method (taking APM32F003F6P6 chip as an example) is as follows:

Open the downloaded pack under "APM32F00x_SDK_v1.2\Package".

Figure 3 Select the pack File in the SDK File

| APM32F00x > APM32F00x_SDK_v1.2 > Package | | v C ∧ | Search Package |
|--|------------------|--------------------|----------------|
| Name | Date modified | Туре | Size |
| 🕎 Geehy.APM32F00x_DFP.1.0.3.pack | 30/10/2020 18:35 | uVision Software P | 1,156 KB |

Install the Pack (double-click the left mouse button) to Pack installation directory of Keil (generally the default path is ok).

|--|

| Pack Unzip: Geehy APM32F00x_DFP 1.0.3 | | × |
|--|-------------------------|--------|
| Welcome to Keil Pack Unzip Release 10/2020 | | |
| This program installs the Software Pack: | | |
| Geehy APM32F00x_DFP 1.0.3 Geehy Semiconductor APM32F00x Series Device Support | t, Drivers and Examples | |
| Destination Folder | | |
| Keil Pack Unzip — Pack already installed. Click "Next" to replace. | << Back Next >> | Cancel |

Wait until the installation is finished.



Figure 5 Example of Installation Finished

| Pack Unzip: Geehy APM32F00x_DFP 1.0.3 | × |
|--|---|
| Keil Pack Unzip completed | |
| Geehy APM32F00x_DFP 1.0.3 | |
| Keil Pack Unzip has performed all requested operations successfully. | |
| | |
| | |
| | |
| | |
| Keil Pack Unzip | _ |
| << Back Finish Cance | ! |

3.1.2 Configuring the Geehy-Link debugger

Open the project file

"APM32F00x_SDK_v1.2\Examples\SysTick\SysTick\Project\MDK\SysTick.uvprojx" and then select the "Options for Target" tab.

Figure 6 Select the "Options for target..." Tab



In the "Debug" option, download and select the simulator as "CMSIS-DAP Debugger" and then select "Setting".



| W Options for Target 'SysTick' | × |
|--|---|
| Device Target Output Listing User C/C++ Asm O Use Simulator with restrictions Settings Limit Speed to Real-Time V Load Application at Startup V Run to main() Initialization File: Restore Debug Session Settings V Breakpoints V Toolbox V Watch Windows & Performance Analyzer | Linker Debug Utilities Image: Ulink2/ME Cortex Debugger Settings ULINK2/ME Cortex Debugger ULINK2/ME Cortex Debugger ULINK2/ME Cortex Debugger ULINKPIO Cortex Debugger Image: UlinkEpide Debugger main() Initializatio J-LINK / J-TRACE Cortex Models Cortex-M Debugger Edit St-Link Debugger Edit NULink Debugger Pemicro Debugger Verticate SiLabs UDA Debugger Altera Blaster Cortex Debugger Image: Verticate Windows Image: Tracepoints |
| Image Component View Image Component View | Image: System Viewer Driver DLL: Parameter: SARMCM3.DLL Image: System Viewer Dialog DLL: Parameter: TARMCM1.DLL -pCM0+ Image: Warn if outdated Executable is loaded ewer Description Files |
| OK Can | ncel Defaults Help |

Figure 7 Select "CMSIS-DAP Debugger" and then Select "Settings"

In the "CMSIS-DAP Cortex-M Driver Setup" interface, select "Geehy CMSIS-DAP" (the old version is "Apex CMSIS-DAP") and click "OK". The emulator configuration ends here.

Figure 8 Select "Geehy CMSIS-DAP" and Click "OK"

| CMSIS-DAP Cortex-M Target Driver | Setup | × |
|--|---|----------|
| Debug Trace Flash Download | | |
| CMSIS-DAP - JTAG/SW Adapter | SW Device | |
| Geehy CMSIS-DAP | IDCODE Device Name | Move |
| Any Geeby CMSIS-DAP | SWDIO Ox0BC11477 ARM CoreSight SW-DP | Up |
| Firmware Version: 1.2 | | Down |
| SW.I Port SW | Automatic Detection ID CODE: | |
| | C Manual Configuration Device Name: | |
| | Add Delete Update AP: 0 | ×00 |
| Connect & Reset Options | Cache Options Download Option | s |
| Connect Normal Rese | t Autodetect 💽 Cache Code 🗌 Verify Code D |)ownload |
| Image: Provide the set of the s | op after Reset | |
| | OK Cancel | Help |

3.1.3 Configuring Geehy-Link debugger under IAR EW for Arm

Open the project file "APM32F00x_SDK_v1.2\Examples\SysTick\SysTick\Project\IAR\SysTick.eww"



and then select the "Options" tab.



SysTick - IAR Embedded Workbench IDE - Arm 9.20.2 File Edit View Project J-Link Tools Window Help 🗸 < Q, > 🛸 HE < 📮 > 🖪 🖻 🗋 🌒 🖷 💽 📲 📮 10 C 🛛 🛍 📙 🖉 🖆 10 C Workspace ▼ ¤ × Debug \sim Files ø 🖃 🌒 SysTick - De Opti 🕂 🛨 🛋 Application Make — 🕀 🧉 Board — 🕀 🧉 CMSIS Compile — 🕀 🧉 StdPeriphD Rebuild All └─── **=** Output Clean C-STAT Static Analysis > Stop Build Add > Remove Rename... Version Control System > Open Containing Folder... File Properties... Set as Active O TIN

Select "CMSIS DAP" simulator in "Debugger" and click "OK".

| Figure | 10 Select | "CMSIS-DAP' | and Click "OK" | |
|--------|-----------|-------------|----------------|--|
| | | | | |

| ategory: | | Factory Settings |
|--------------------|--|------------------|
| eneral Options | | |
| atic Analysis | | |
| Intime Checking | Sotup Developed Income Multicome Extra Options Divisio | |
| C/C++ Compiler | Setup Download Images Multicore Extra Options Pluging | 6 |
| ssembler | | |
| Output Converter | Driver 🔽 Run to | |
| Custom Build | | |
| uild Actions | J-Link/J-Trace V main | |
| inker | Simulator | |
|)ebugger | CADI | |
| Simulator | CMSIS DAP | |
| CADI | GDB Server | |
| CMSIS DAP | I-jet | |
| GDB Server | J-Link/J-Trace | |
| I-jet | TI Stellaris | |
| J-Link/J-Trace | Nu-Link | |
| TI Stellaris | PE micro | |
| Nu-Link | ST-LINK | |
| PE micro | Third-Party Driver | |
| ST-LINK | TI MSP-FET | |
| Third-Party Driver | TI XDS debugger\Geehy\APM32F003F | 6.ddf |
| , TI MSP-FET | | |
| TI XDS | | |
| | | |



4 APM32PROG tutorial

4.1 APM32PROG software upper computer

Download the upper computer software Apex-APM32-Setup.msi, and then install it (the installation interface is omitted). Open the software and connect APM32PROG. After successful connection, the upper computer will read the firmware version of the current APM32PROG, as shown in the red box at the bottom of the figure below. If the firmware version of the current APM32PROG is lower than the latest version, you will be prompted to update the firmware version.

| APM32 PROG UI | | | - 🗆 X |
|--|-------------|---------------------|----------------|
| Configuration Firmware About Language | | | |
| | | Open | Merge File |
| Configuration | | | |
| Product-Series APM32F101 | ~ | DownLoa | d(Offline) |
| Product-Type APM32F101C4T6 | ~ | Operati | on(Online) |
| External-Power Power OFF | ~ | | |
| Comm-Protocol SWD | ~ | BaseAddress: | 0x08000000 |
| StartAdd-of-Program Ox 0800 0000 | | Flash: DeviceID: | 16K 0x412 |
| Operation-Mode Program | ~ | | |
| 🗌 Page-Erase | | -Remain-Times(Off) | line Download) |
| Offline-DownLoad-Time | | | Read |
| 🗌 Serial-Number | | | |
| SN-Configuration | | | |
| Address: 0x 0800 0000 Start-SN: 0x | 1 | StartAddress: | 0x 0800 0000 |
| Increment: 0x 1 | | Length(Bytes) | : 0x 0000 4000 |
| Default-Confi Write-Config Re | ead-Config | Read | (Online) |
| Operation-Record | | | |
| FW Version: 00.262022/3/15 17:07:21 | | | |
| FW Version: 00.262022/3/15 17:07:55 FW Version: 00.262022/3/15 17:08:01 | | | Clear |
| | | | Save |
| Current-FW-Version: 00.26 | Latest-FW-V | ersion: 00.26 | |

Figure 11 APM32 PROG Functional Area

The functional area of the APM32PROG upper computer is divided into the file opening area, configuration area (including serial number configuration and configuration writing), and offline or online operation area. APM32PROG, together with the upper computer, can realize many functions such as programming, reading, encryption and decryption of the target MCU. The following takes APM32F003F6P6 as an example to introduce the methods of offline downloading and online reading MCU of APM32PROG. Readers of other functions can explore according to the software help and the instructions for use.



4.2 APM32PROG offline programming

Our chip is APM32F003F6P6, which only supports SWD debugging. Our APM32PROG configuration is shown in the figure below.

| | | | | | - | |
|--------|----------|----------|---------|------|---------|--------|
| Elaura | 10 10100 | | ADM322 | | Configu | rotion |
| гюше | | -0035656 | APIVIOZ | PRUG | Comiau | auon |
| | | | | | | |

| APM32 PROG UI | | | - | - 0 X |
|--------------------------------------|--|----------------|-----------------------|-------------|
| Configuration Fi | irmware About Language | | | |
| | | | | |
| 2\Examples\SysTic | ck\SysTick\Project\MDK\Objects\ | SysTick.hex | Open | Merge File |
| Configuration | | | | |
| Product-Series | APM32F003 | \sim | DownLoad(O | ffline) |
| Product-Type | APM32F003F6P6 | ~ | Operation(| Online) |
| External-Power | Power ON | ~ | | |
| Comm-Protocol | SWD | ~ | BaseAddress: 0x0 | 0000000 |
| StartAdd-of-Prog | ram 0x 0000 0000 | | Flash: 32K | |
| Operation-Mode | Program | ~ | DeviceID: 0x0 | |
| | Page-Erase | | -Remain-Times(Offline | e Download) |
| Offline-DownLo | oad-Time1 | | | Read |
| 🗌 Serial-Number | | | | |
| SN-Configuratio | n | | | |
| Address: Ox | 0000 0000 Start-SN: 0x | 1 | StartAddress: 0x | 0000 0000 |
| Increment: Ox | 1 | | Length(Bytes): 0 | K 0000 8000 |
| Default-Confi | Write-Config Read | H-Config | Read(On) | ine) |
| Operation-Record | | | | |
| The file is OK | 2022/3/15 17:17:45 | | | |
| Write the config Write the config | guration is OK2022/3/15 17: guration is OK2022/3/15 17: | 18:14 18:20 | | Clear |
| Offline-OK20 | 022/3/15 17:18:20 | | | Save |
| | | | | |
| urrent-FW-Version | : 00.26 | Latest-FW-Ve | ersion: 00.26 | |

Select the hex (or bin) file to be downloaded,

- 1. Product-Series APM32F003
- 2. Product-Type APM32F003F6P6

At this time, the APM32PROG upper computer will pop up the window of configuring the option bytes. Readers who need to configure MCU option bytes can use this function. This function is not used for this offline programming, so it is omitted here.



| 🚪 Configuratio | n Option Byte | | | | | - | - 0 | × |
|----------------|--------------------------|-----------|----|----|---------|-------------|-----------|---|
| | | | | | confirm | | | |
| Trite Og | otion Byte | | | | | Address | [31:0] | |
| קתק | wor per ^y ser | data Ox | चच | चच | | 0x00020400: | FFFF5AA5 | |
| | | | гг | гг | | 0x00020404: | FFFFFFFFF | |
| WDG_SW | U IWDG_SW | WRP Ox | FF | FF | FF | 0x00020408: | FFFFFFFF | |
| LIRCEN | HIRCTRIM | AFR1 Ox | FF | FF | | 0x0002040C: | FFFFFFFF | |
| | | CLKOPT 0x | FF | FF | | 0x00020410: | FFFFFFFF | |
| | | | | | | 0x00020414: | FFFFFFFF | |

Figure 13 Window of Configuring Option Byte

- 3. Select Power ON for External-Power
- 4. Select SWD for Comm-Protocol
- 5. Set BaseAddress to 0x00000000
- 6. Select Program for Operation-Mode

Serial-Number is not required and is not configured temporarily. After the above configuration is set, click "Write-Config" to write the configuration to APM32PROG. Then click "DownLoad (Offline)" to download the program to APM32PROG. At this time, the configuration information and program have been downloaded to APM32PROG. Next, you only need to supply power to APM32PROG, connect the target chip, then click the programming start button, wait until the programming prompt light turns green and the offline programming work is completed.

4.3 APM32PROG online reading

APM32PROG can not only complete the offline operation of the target MCU, but also perform online reading or other operations on the MCU. This section will take APM32F003F6PU as an example to read the program burned in the previous section.

The parameter configuration is summarized in the previous 2.2 in the read setting interface

- 1. Set StartAddress to 0x00000000
- 2. Set Length(Bytes) to 0x00008000

The last selected operation is "Read (Online)" (APM32PROG needs to connect the target MCU and PC) to read the content that the start address of the target MCU is 0x00000000 and the size is 0x8000 Bytes.



| | | Open Merge File |
|------------------|--------------------------|--------------------------------|
| Configuration | | |
| Product-Series | APM32F003 | > DownLoad(Offline) |
| Product-Type | APM32F003F6P6 | V Operation(Online) |
| External-Power | Power ON | × |
| Comm-Protocol | SWD | BaseAddress: 0x00000000 |
| StartAdd-of-Pros | gram 0x 0000 0000 | Flash: 32K |
| ····· | Providence | DeviceID: 0x0 |
| operation-mode | □ Page-Erase | Remain-Times(Offline Download) |
| Offline-DownL | oad-Time1 | Read |
| | | |
| -SN-Configuratio | n | |
| Address: 0x | 0000 0000 Start-SN: 0x 1 | StartAddress: 0x 0000 0000 |
| Increment: 0x | 1 | Length(Bytes) : 0x 0000 8000 |
| Default-Confi | Write-Config Read-Config | Read(Online) |
|)peration-Record | 1 | |
| | | Clear |
| | | |

Figure 14 Read the Burnt Program

Wait to read the progress bar.

Figure 15 Read Progress Bar

| 🤣 Waiting | × |
|-----------|---|
| | |
| 33% | |
| | |
| | |

After reading successfully, the data pane interface will pop up, and you can click the "Save" button to save the read data.



Figure 16 Data Pane Interface

| 🕜 AP | M32 PROG | G UI | | | | | | | | | | | | | | | | | | | _ | | | > | < |
|--------|-------------|------|------|----------|----------|-------|----------|----------|-----|----------|-----|-----|----------|----------|------|----------|-----------|-------|---|---|---|------|------|----|---|
| Conf | iguration | Fi | irmı | vare | э. | Abo | ut | La | ngu | lage | | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | | | | | | | | _ | | | | |
| | 🔗 MCU Da | ata | | | | | | | | | | | | | | | | | _ | - | | | × | | J |
| -00 | | | | | | | | | | | | | | | | | | | | | | | | ۱I | |
| Pr | | | | | | | | | | | | | | | | | | | | | | Save | • | ш | |
| | | ~~ | ~1 | ~~ | ~~ | ~ • | <u> </u> | ~~ | ~ 7 | ~~ | ~~~ | ~ * | 0.0 | ~~ | 0.0 | 0.0 | 0E | | | | | | | 1 | |
| Pr | 00000000 | 00 | 01 | 02 | 20 | 04 | 05 | 00 | 07 | ប8 ធា | 09 | 00 | 00 UB | UC RR | 00 | UE 00 | 00 | | | | | | 11 | | |
| Бv | 000000010 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | | | | | | | | |
| EA | 00000020 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 4D | 05 | 00 | 00 | | | | | | | | |
| Co | 00000030 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | FЗ | 04 | 00 | 00 | 51 | 05 | 00 | 00 | | | | | | | 1 | |
| | 00000040 | 00 | 00 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | | | | | | | | |
| St | 00000050 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | 00 | 00 | 00 | 00 | | | | | | | | |
| | 00000060 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | | | | | | | | |
| Op | 00000070 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | 00 | 00 | 00 | 00 | | | | | | | | |
| | 00000080 | 00 | 00 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | | | | | | | | |
| | 00000090 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | | | | | | | | |
| | 0000000A0 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | | | | | | | | |
| _ | 00000080 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 00 | 79 | 01 | 00 | 10 | 00 | 10 | 02 | F8 46 | | | | | | | | 2 |
| | 00000000 | 00 | 10 | 3E AD | 10 | UC EA | AO AG | 30 ED | 10 | 08 | 30 | 24 | 10 D1 | 20 | 10 | 82 | 40 E0 | | | | | | | | |
| - 5 | 000000000 | 707 | 16 | AD | 90 20 | 04 | 40 CC | DD DA | 40 | AU 01 | 942 | 22 | 12 | 00 | DO | 20 | ГО 1 А | | | | | | | | |
| | 00000000000 | 12 | 40 | 1R | 3E 46 | 22 | 43 | 18 | 40 | 101 | 05 | 00 | 44 | 00 C0 | 05 | 00 | 100 | | | | | | | | |
| | 000000100 | 10 | 34 | 02 | тз | 78 | C8 | 78 | C1 | R1 | D8 | 52 | 07 | 01 | D3 | 30 | C8 | | | | | | | | |
| | 00000110 | 30 | C1 | 01 | D5 | 04 | 68 | 0C | 60 | 70 | 47 | 00 | 00 | 00 | 23 | 00 | 24 | | | | | | | | |
| | 00000120 | 00 | 25 | 00 | 26 | 10 | 3A | 01 | D3 | 78 | C1 | FB | D8 | 52 | 07 | 00 | D3 | | | | | | | | |
| | 00000130 | 30 | C1 | 00 | D5 | 0B | 60 | 70 | 47 | 1F | B5 | 1F | BD | 10 | B5 | 10 | BD | | | | | | | | |
| L | 00000140 | 00 | FO | ΕO | F8 | 11 | 46 | FF | F7 | F7 | FF | 00 | FO | 67 | FA | 00 | FO | | | | | | | | |
| | 00000150 | F8 | F8 | 03 | Β4 | FF | F7 | F2 | FF | 03 | BC | 00 | FO | FF | F8 | 00 | 00 | | | | | | | | |
| On | 00000160 | 09 | 48 | 80 | FЗ | 08 | 88 | 09 | 48 | 80 | 47 | 09 | 48 | 00 | 47 | FE | E7 | | | | | | | | |
| | 00000170 | FE | E7 | FE | E7 | FE | E7 | FE | E7 | FE | E7 | 00 | 00 | 05 | 48 | 06 | 49 | | | | | | | | |
| Ψı | 00000180 | 06 | 4Å | 07 | 4B | 70 | 47 | 00 | 00 | 68 | 06 | 00 | 20 | 61 | 05 | 00 | 00 | | | | | | | | |
| Or | 00000190 | BD | 00 | 00 | 00 | 68 | 00 | 00 | 20 | 68 | 06 | 00 | 20 | 68 | 02 | 00 | 20 | | | | | | | | |
| | | | | | | | | | | | | | | | | | | | | | | | | 1 | |
| | | | | | | | | | | | | | | | | | | | | | | | Save | | |
| Currer | nt-FW-Vers | ion | : 0 | 0.2 | 6 | | | | | | | | La | ates | t-F۱ | W-\ | /ersion: | 00.26 | | | | | | | |



5 APM32 ISP Multiport Programmer & DFUProgrammer

5.1 About BootLoader of APM32 MCU

A segment of BootLoader program and system storage area are built in APM32F0/1/4 and APM32E1 series MCU before they leave the factory, to facilitate users to implement program downloading, option bytes programming and other operations on MCU through ISP. The BootLoader of each chip has different communication methods to support upgrade.

| General MCU series | Whether supporting ISP upgrade | Supporting interfaces | BootLoader entry mode |
|--|--------------------------------|---|---|
| APM32F003x4x6 | No | - | - |
| APM32F030x8/051x8 | Yes | USART (PA9/PA10、 PA14/PA15) | Boot0 connects to 1 |
| APM32F030xC/091xC | Yes | USART (PA9/PA10、 PA14/PA15) I2C | Boot0 connects to 1 |
| APM32F072xB | Yes | USB (PA11/PA12) USART (PA9/PA10、 PA14/PA15) I2C (PB6/PB7) | Boot0 connects to 1 |
| APM32F103xBxCxE | Yes | USART (PA9/PA10) | Boot0 connects to 1, and Boot1 connects to 0 |
| APM32F405xG 407xExG APM32F415xG 417xExG | Yes | USB (PA11/PA12) USART (PA9/PA10、 PB11/PB12、PC11/PC12) CAN (PB5/PB13) | Boot0 connects to 1, and Boot1 connects to 0 |
| APM32E103xE | Yes | USART (PA9/PA10) | Boot0 connects to 1, and Boot1 connects to 0 |

Table 1 Communication Mode of Each Chip

This brief summary will introduce the basic chip connection and programming methods for the PC-side tools of commonly used USART and USB. More information can be obtained from the official website or technical support engineers of Geehy.

5.2 APM32 ISP Multiport Programmer

This brief summary will take APM32F103VBT6 chip as an example to introduce the basic function of



APM32 ISP Multiport Programmer -- chip programming and erasing. The software can be obtained from the official website of Geehy.

5.2.1 Preparation before connecting MCU

Since the communication mode between APM32 ISP Multiport Programmer and MCU is USART, here we need to use USB-to-TTL module to connect PC to MCU. The wiring method is as follows.

| | Table 2 Wiring Method | | | | | | | | | | |
|------|-----------------------|----------|-------------------------|--|--|--|--|--|--|--|--|
| PC | USI | B to TTL | APM32F103VBT6 MINIBOARD | | | | | | | | |
| | | ТХ | PA10 | | | | | | | | |
| | LICR | RX | PA9 | | | | | | | | |
| 0.28 | USB | GND | GND | | | | | | | | |
| | | VCC | VCC | | | | | | | | |

After wiring, install the corresponding USB-to-TTL module driver. After the driver is installed normally, you will see the corresponding "USB Serial Port" port in the "Device Manager".

Figure17 "USB Serial Port" Port in "Device Manager"

| > | | Memory technology devices |
|---|----|---------------------------------|
| > | | Mice and other pointing devices |
| > | | Monitors |
| > | P | Network adapters |
| > | •? | Other devices |
| ~ | | Ports (COM & LPT) |
| | | USB-SERIAL CH340 (COM3) |
| > | | Print queues |

5.2.2 APM32 ISP Multiport Programmer connects to MCU

As shown in the table in Section 5.1, "Boot0 connects to 1 and Boot1 connects to 0" is required so that APM32F103VBT6 can enter BootLoader (MCU needs to be reset when MCU is powered on before the jumper cap is reconnected). After all wiring operations are completed, open the installed APM32 ISP Multiport Programmer (the installation process is omitted).

The general process of connecting APM32 ISP Multiport Programmer to MCU is as follows,

1. Click the plus sign Add New Window below the main interface to get the setting window

2. Select the corresponding system USB Serial Port under the COM in the setting window. Here is COM3



- 3. Set the required baud rate under the Bandrate in the setting window. Here it is set to 115200
- 4. Set the connection timeout under the Timeout in the setting window. It is set to 2 here
- 5. Finally, click the Connect button under Setting to complete the connection to the target MCU

| 🙁 Gee | ehy ISP Multipo | ort Prog | grammer | | | | | | | | | _ | | × |
|---------|-----------------|----------|-----------|--------|--------|----------|-------|------|--------|------|------------------|------------|-------------------|------------|
| File | Help Wind | ows (| Operation | Help | Lang | guage | | | | | | | | |
| + 3 | 😤 🔕 I 🕥 🔾 | | | | | | | | | | | | | |
| Devicel | nfo | | | _ | | | | | | | | | | џ |
| Pr | Setting | COM | | Bandra | te | Timeout | | Chip | | P F | ¥ Ver | Progr | ess | |
| 0 | Connect | COM3 | ~ | 115200 | \sim | 2 | - | | \sim | | | | 0% | |
| | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | |
| Prod | luct:0 | | | | | | | | | | | | | ▼ X |
| ¥rite | Read | | | | | | | | | | | | | |
| Er | ase Setting | s | | | | | | | | | | | | |
| 0 | Erase usage s | pace | 🔘 Mass | erase | | 🔵 Page e | rase | Sta | rt pag | ge O | * | End pa | ge 11 - | * |
| - Do | wnload File | | | | | | | | | | | | | |
| Star | t Addr 0X 8000 | 0000 | D:\桌面\测 | 试\测试i | 卖出\; | 测试用32K了 | と件.bi | in | | | | | Select | |
| - V Tr | ite Option 1 | Byte - | | | | | | | | - C | ption | bytes- | | |
| 🗌 R | DP 🗌 n | RST_STI |)BY | | U | ser data | Ox FF | FF | | | Addres Dx1FFF | s F800: | [31:0] 07F85AA | 5 |
| 🗌 n | RST_STOP 🗌 W | DG_SW | | | | WRP Ox | FF | FF | FF | | Dx1FFF | F804: | OOFFOOF | F |
| Do | wnLoad Sett: | ings — | | | | | | | | | Dx1FFF | F808: | OOFFOOF | F |
| IJ | ump to the us | er pros | gram | | | | 🗌 Ve: | rify | | (|)x1FFF] | F80C: | OOFFOOF | F |
| | | | | | | | | | | | | | | |
| | | Stop | | | | | | | | E | xecute | | | |
| | | | | | | | | | | | | | | |
| Execute | e Result | | | | | | | _ | | | | | | д. |
| The ope | eration name | | | | | Resu | lt | | | | | | | |
| | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | |

Figure 18 Connect MCU

After successful connection, the information of the currently connected chip and some operable options will be displayed in the same interface.



| 😟 Geehy ISP Multiport Programmer | | | - 🗆 X |
|-----------------------------------|-----------------|------------|----------------------|
| File Help Windows Operation Help | Language | | |
| + 😂 🚰 🔕 📀 🔾 | | | |
| DeviceInfo | | | |
| Pr Setting COM Ban Ti Chip | P FW Ve: | r Progress | 0% |
| | | <u></u> | |
| | | | |
| | | | |
| | | | |
| Product:0 | | | • |
| Write Read | | | |
| ✓ Erase Settings | 0.5 | | |
| Erase usage space | ○ Page erase | Start page | U End page 127 두 |
| 🖌 Download File | | | |
| Start Addr OX 8000000 D:\桌面\测试\测试 | 读出\测试用32K文件.bir | 1 | Select |
| ✓ ▼rite Option Byte | | | Option bytes |
| | User data on AA | | Address [31:0] |
| | User data UX AA | AA | 0x1FFFF800: 00FF5AA5 |
| 🔽 nRST_STOP 🔽 \DG_S\ | WRP Ox FF | FF FF FF | 0x1FFFF804: 55AAAAAA |
| Download Sattings | | | Ox1FFFF808: FFFFFFFF |
| Ump to the user program | U vor | ; f., | 0x1FFFF80C: FFFFFFFF |
| | | , | |
| | | | _ |
| Stop | | | Execute |
| | | | |
| Execute Result | | | |
| The operation name | Result | | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |

Figure 19 Information and Operable Options of the Currently Connected Chip

5.2.3 APM32 ISP Multiport Programmer download program

In Section 5.2.2, after successfully connecting APM32F103VBT6 chip, we can see that there are many operable items, such as: under Erase Setting, you can choose full erase or sector erase. Select the downloaded file In Download File; choose to operate the option bytes in Write Option Byte; in Download Setting, some operations after downloading can be selected.

Here we choose to download the program of APM32F103VBT6, adopt full erase, and do not operate the option byte, and after downloading, verify and execute the program. Relevant setting interface is shown below.



Figure 20 Setting Page

| Product:0 | | |
|------------------------------------|--|----------------------|
| ite Read | | |
| 2 Erase Settings | | |
| 🔿 Erase usage space 🛛 🔾 Mass era | ase O Page erase Start page | e 0 🌲 End page 127 🜲 |
| 🖉 Download File | | |
| Start Addr 0X 8000000 C:\Users\784 | 60\Desktop\Geehy\APM32F10x_SDK_V1.6\Ex | amples\SysTic Select |
| Trite Ontion Byte | | Ontion hytes |
| sires operan by co | | Address [31:0] |
| RDP RRST_STDBY | User data Ox AA AA | 0x1FFFF800: 00FF5AA5 |
| 🔽 nRST_STOP 🔽 WDG_SW | WRP Ox FF FF FF FF | Ox1FFFF804: 55AAAAAA |
| | | 0x1FFFF808: FFFFFFFF |
| ✓ DownLoad Settings | | 0x1FFFF80C: FFFFFFF |
| < Jump to the user program | 🔽 Verify | |

After the above options are checked, click "Execute" to execute our settings. Wait and when the progress bar is finished, we can view our operation log in the "Execute Result" window.

Figure 21 "Execute Result" Window

| Execute Result | | | | д |
|--------------------|-----------|--------------------------|---------|---|
| The operation name | | | Result | |
| 2022/3/16 15:52:36 | Product:0 | Mass erase | Success | |
| 2022/3/16 15:52:37 | Product:0 | Download File | Success | |
| 2022/3/16 15:52:37 | Product:0 | Verify | Success | |
| 2022/3/16 15:52:37 | Product:0 | Jump to the user program | Success | |
| | | | | |
| | | | | |
| | | | | |

5.3 DFUProgrammer

The function of this software is basically similar to that of APM32 ISP Multiport Programmer, except that it supports using the USB as the software to upgrade MCU of peripherals. This brief summary will use APM32F407IGMINIBOARD to demonstrate the chip program reading function of DFUProgrammer.

5.3.1 Preparation before connecting MCU

The installation package can be obtained on the official website of Geehy. It includes the installation instructions of the software.

After the driver is correctly installed, connect APM32F407IGMINIBOARD "Boot0 connects to 1 and Boot1 connects to 0" (MCU needs to be reset when MCU is powered on before the jumper cap is reconnected). Use USB cable to connect PC and development board. At this time, we will see the "APM32 Bootloader" device under "Universal serial bus controller" on the device manager.

Figure 22 "APM32 Bootloader" in the "Device Manager" Interface



| 🛔 Device Manager | |
|---------------------------------------|---|
| File Action View Help | |
| | |
| > 🥅 Keyboards | ^ |
| > 🕼 Mice and other pointing devices | |
| > 🛄 Monitors | |
| > 🐙 Network adapters | |
| > 👔 Other devices | |
| > 🛱 Ports (COM & LPT) | |
| > 🚍 Print queues | |
| > 🚍 Printers | |
| > Processors | |
| > 📑 Software components | |
| > Software devices | |
| > 💵 Sound, video and game controllers | |
| > 🍇 Storage controllers | |
| > 🏣 System devices | |
| ✓ | |
| APM32 Bootloader | |
| 🏺 Generic SuperSpeed USB Hub | |
| 🔋 🔴 Generic USB Hub | |

5.3.2 DFUProgrammer connects to MCU

Open DFUProgrammer software. If the computer has been connected to an "APM32 Bootloader" device, the initial interface is shown below. If no device is found, click the "Search device" button to search for the device. Click the "Connect a device" button to connect to the target MCU.

| DFU Programmer V1 | .0.1 | | | | ,∗ Ō × |
|---|---|---|--|------------------------------------|--------|
| Hanguage | Port: USB1 | ▼ PID: 0x106 | VID: 0x314B Conn | ect Search | |
| ⊥ Update ☑ Option Bytes ☆ Erasing | Device: Flash Size: Factory: Type: | APM32 ISP DFU mode 1 M Geehy APM32F407 | Product ID: Version: Serial Number: Size: | 0x106 2.0 65313223864E 64 | |
| 용 User Manual | Log | | | | |

Figure 23 Main Interface of DFUProgrammer Software

After the target MCU is successfully connected, we can see the corresponding prompt in the log interface.



| DFU Programmer V1. | 0.1 | | | | ,× ⊡ × |
|--------------------|--|---|---|--|---------------|
| DFU Programmer V1. | Device: Flash Size: Factory: Type: Log | PID: 0x106 APM32 ISP DFU mode 1 M Geehy APM32F407 | VID: 0x314B Disco Product ID: Version: Serial Number: Size: | nnect Search 0x106 2.0 65313223864E 64 | |
| | [2022-08-01 13:59:2 | 3.2062] INFO Search device c | omplete. vice successfully. | | ل لی لی |

Figure 24 Status of Device Successfully Connected

5.3.3 DFUProgrammer read program

Click the "Read a chip" button on the left to enter the chip content reading interface. In this interface, you can set the content and size of read chip, click the "Read chip" button to read the corresponding data, and click the "Save data" button to save the data to PC.

| Connect Address 0 Read 0X0800000 20000 Update 0X0800010 08000 | 4 88 08000231 | 8 | С | ASSCIL |
|---|--|----------|----------|----------|
| Read 0X0800000 20000 Update 0X08000010 08000 | 88 08000231 | | | |
| Update 0X08000010 08000 | | 08000427 | 0800041F | ? 1' |
| opdate | 23 08000365 | 080005A5 | 00000000 | #e? |
| 0X08000020 00000 | 00000000 | 00000000 | 0800043D | = |
| Option Bytes 0X08000030 08000 | 69 0000000 | 08000429 | 0800043F | i)? |
| Erasing | AD 000000AD | 00000040 | 00000340 | 1/1/1/1/ |
| User Manual Log [2022-08-01 13:59:23.2062] INFO | earch device complete. onnect the device succes | ssfully. | | |

Figure25 Read a Chip



Figure 26 Save Data to PC

| 😧 Save As | | | | | |
|----------------|-------------------------------|-----------------------------|-----------------|--------|--------|
| ← → • ↑ 📘 | → This PC → desktop → Geehy → | DFUProgrammer 🗸 🖑 | Search DFUProgr | ammer | Q |
| Organize 🔻 Ne | ew folder | | | | ? |
| This PC | ^ Name ^ | Date modified | Туре | Size | |
| 3D Objects | | No items match your search. | | | |
| 📃 desktop | | | | | |
| Documents | | | | | |
| 👆 Downloads | | | | | |
| b Music | | | | | |
| Pictures | | | | | |
| 📑 Videos | ~ < | | | | > |
| File name: | DFUProgrammer_read | | | | ~ |
| Save as type: | bin文件(*.bin) | | | | \sim |
| ∧ Hide Folders | | | Save | Cancel | |

After the data is saved successfully, you can get the information of data successfully saved in the log window.

| 2 Language | Start address: 0x0 | 8000000 16 bit O 32 bit | Size: 0x400 | 🗘 Read | 🛱 Save | |
|---------------|----------------------------|-----------------------------------|--------------------|----------|----------|--------|
| Connect | Address | 0 | 4 | 8 | С | ASSCII |
| Read | 0X08000000 | 20000688 | 08000231 | 08000427 | 0800041F | ? 1' |
| Update | 0X08000010 | 08000423 | 08000365 | 080005A5 | 00000000 | #e? |
| 2 | 0X08000020 | 00000000 | 00000000 | 00000000 | 0800043D | = |
| Option Bytes | 0X08000030 | 08000369 | 00000000 | 08000429 | 0800043F | i)? |
| > Erasing | 0X08000040 | 0800024B | 0800024B | 0800024B | 0800024B | КККК |
| B User Manual | Log [2022-08-01 14:08:3 | 9.0041] INFO Search | device complete. | | | |
| | [2022-08-01 14:08:4 | 0.1460] INFO Connec | t the device succe | ssfully. | | |
| | [2022-08-01 14:08:4 | 2.2872] INFO read su | cceeded. | | | |

Figure 27 Data Saved Successfully

Then the program reading operation has been completed.



6 Version history

| Table 3 Document Version H | History |
|----------------------------|---------|
|----------------------------|---------|

| Date | Version | Change History |
|--------------|---------|---|
| June 6, 2022 | 1.0 | New |
| Oct 26, 2022 | 1.1 | Fixed the incorrect description of entering ISP mode in Section 5 |



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